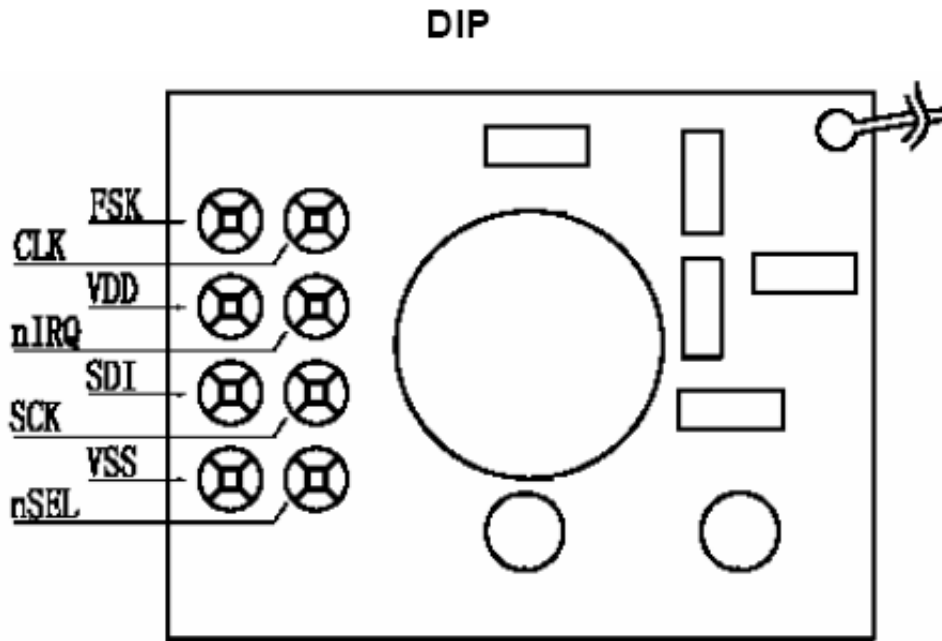


Document Version: 1.5

### Pin Description:



Definition	TYPE	function
FSK	DI	FSK data input
CLK	DO	clock out for MCU (1 MHz-10 MHz)
VDD	S	Positive power supply
nIRQ	DO	Interrupts request output ( active low )
SDI	DI	SPI data input
SCK	DI	SPI clock input
VSS	S	negative power supply, GND
nSEL	DI	Chip select (active low)

Package Size:

DIP PACKAGE (D)

