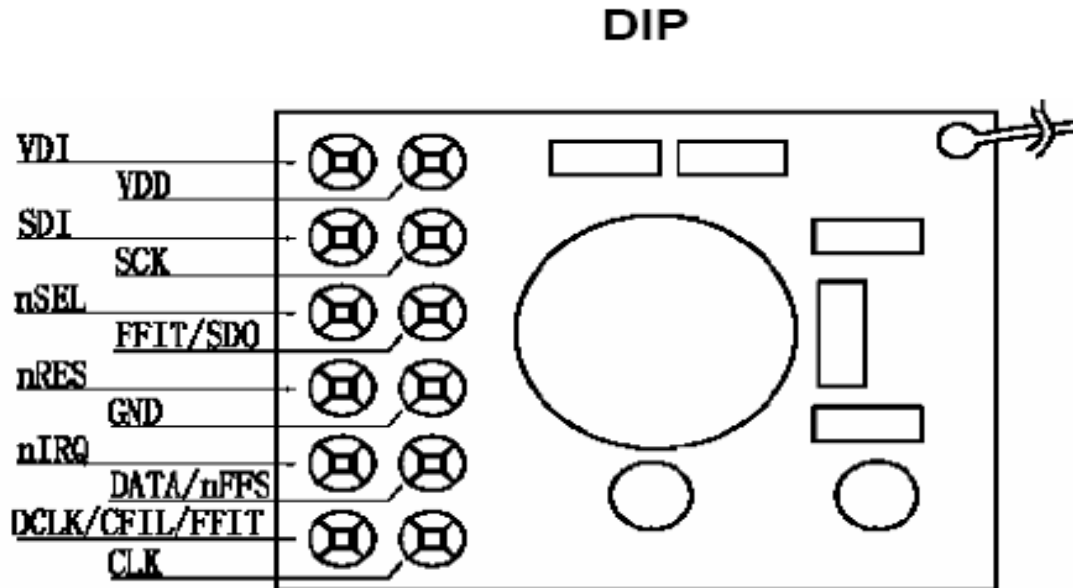


Document Version: 1.5

Pin Description:



definition	Type	Function
VDI	DO	Valid data indicater
VDD	S	Positive power supply
SDI	DI	SPI data input
SCK	DI	SPI clock input
nSEL	DI	Chip select (active low)
FFIT/SDO	DO	FIFO fill interrupt(active low) or status read data output
nRES	DO	Reset output (active low)
GND	S	Power ground
nIRQ	DO	Interrupts request output (active low)
DATA/nFFS	DO/DI	Data input(non FIFO mode)/ FIFO select
DCLK/CFIL/FFIT	DO/AIO/DO	Clock output (no FIFO)/ external filter capacitor(analog mode)/ FIFO interrupts(active high)when FIFO level set to 1, FIFO empty interruption can be achieved
CLK	DO	Clock output for external microcontroller

Package Size:

